In the Claims

The following Listing of Claims replaces all prior versions in the application:

LISTING OF CLAIMS

1. (Currently amended) ~~Process~~ A process for deterministic transmission of asynchronous data in packets, in which data arriving asynchronously is stored in ~~batteries~~ ~~(11)~~buffers as ~~and when~~ it arrives, ~~the~~ said process ~~being typified in that it comprises the following stages~~comprising:

~~reception of~~ receiving data contained in a set of ~~batteries~~ buffers in one or ~~several~~ more packeting modules ~~(13);~~

commencing a first packeting realization cycle in said packeting modules, said packeting realization cycle including, for a first set of packets, start of packeting, packeting with sorting and enhancement of data, end of packeting and sending of packets~~the made-up packet~~,

ending, for said first set of packets, said packet realization cycle in said ~~stoppage of packet make-up in the course of realization in a~~ packeting ~~module (13)~~ modules at the request of ~~when~~ a message composition module ~~(15) needs this packet;~~

forwarding to said message composition module said first set of packets regardless of the state of completion of said first packeting realization cycle;~~transmission of the packet thus made up;~~

commencing a second packeting ~~and start of the~~ realization cycle for a second set of packets;~~of a new packet,~~

recovering ~~recovery~~ one after another ~~of~~ the first set of packets ~~thus created~~, in a predefined order, in the message composition module ~~(15),~~;

setting, in the message composition module, ~~of the~~ a first message comprised of the first set of packets~~, made up in the message composition module (15)~~ to ~~the~~ an electrical format in ~~the~~ a protocol used for ~~the~~ message transmission.

2. (Currently amended) A device ~~Device~~ for deterministic transmission of asynchronous data in packets comprising:

an input module

one or more buffers configured to receive ~~at the least one input module (10) receiving the input data, batteries (11) receiving~~ digital data from the ~~coming from this~~ input module~~,~~;

a plurality of ~~several~~ packeting modules ~~(13) each~~ connected to said one or more buffers ~~at least one battery (11),~~ ;

at the least one control module for ~~battery~~ buffer dump ~~(14)~~ monitored by at least one packeting module ~~(13)~~ of said plurality of packeting modules~~,~~ ;

a message composition module ~~(15)~~ receiving the outputs of said plurality of ~~all the~~ packeting modules ~~(13)~~ for composing a message therefrom, said message composition module configured ~~and able~~ to send to each of said plurality of packeting modules ~~them~~ an order to terminate a packet assembly procedure regardless of whether said packet assembly procedure is completed~~for end of packet make up,~~ ;

a packet formatting module ~~for formatting packets (16)~~configured to format said message from said message composition module; and

, ~~an~~ an output module ~~(17)~~ configured to transmit said message ~~capable of issuing each made-up packet~~ on a transmission line ~~(18)~~.

3. (Currently amended) The process of ~~Use of the process according to~~ claim 1, further comprising conducting ~~in~~ data acquisition and real-time processing ~~systems~~ for test installations of new aeroplanes.